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# THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )
)
ELIYAHOU HARARI, ROBERT D. )
NORMAN and SANJAY MEHROTRA )
)
Serial No.: 08/771,708 )
)
Filed: December 20, 1996 )
)
For: FLASH EEprom SYSTEM )
)

Group Art Unit: 2785

Examiner: Ly V. Hua

San Francisco, California

Assistant Commissioner for Patents
Washington, D.C. 20231

## RESPONSIVE AMENDMENT

Sir:

In response to the Office Action dated March 31, 1998, please amend the above-identified application, as follows:

IN THE SPECIFICATION:

Page 1, delete the entire line 1 that contains the title and substitute the following new title therefor: --FLASH EEPROM SYSTEM WITH CELL BY CELL PROGRAMMING VERIFICATION--

IN THE CLAIMS:

Cancel claim 74 without prejudice, and amend claims 83 and 98, as follows:

20 83. (Amended) The memory system according to claim 82, wherein the plurality of addressed cells are less [that] than a number of cells within the individual blocks.

35 98. (Twice Amended) The method of claim 97, wherein resetting at least the cells within said chunk includes initially driving at least all of the cells within said chunk to a common state different than any of said more than two programmable states, and then programming all of at least the cells within said chunk from the common state into [said] one of the more than two programmable states by the following:

applying electrical programming parameters in parallel to at least the cells within said chunk,

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